

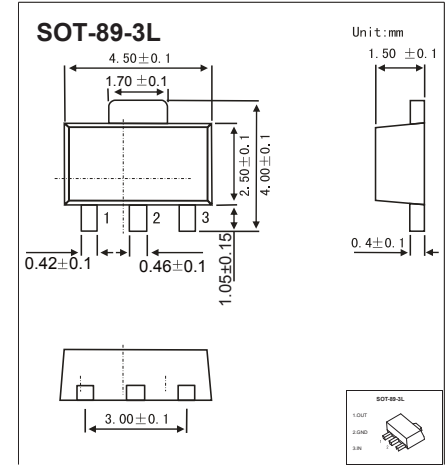
Three-terminal positive voltage regulator

FEATURES

- Maximum output current IOM: 0.1 A
- Output voltage V_O : -12V
- Continuous total dissipation
 P_D : 0.625W ($T_a = 25^\circ\text{C}$)

MECHANICAL DATA

- Case: SOT-89 Small Outline Plastic Package
- Polarity: Color band denotes cathode end
- Mounting Position: Any



ABSOLUTE MAXIMUM RATINGS

(Operating temperature range applies unless otherwise specified)

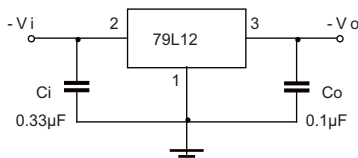
| Parameter | Symbol | Value | Unit |
|---|-----------------|----------|---------------------------|
| Input Voltage | V_i | -35 | V |
| Thermal Resistance from Junction to Ambient | $R_{\theta JA}$ | 200 | $^\circ\text{C}/\text{W}$ |
| Operating Junction Temperature Range | T_{OPR} | 0~+150 | $^\circ\text{C}$ |
| Storage Temperature Range | T_{STG} | -65~+150 | $^\circ\text{C}$ |

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JUNCTION TEMPERATURE ($V_i = -19\text{V}$, $I_o = 40\text{mA}$, $C_i = 0.33\text{F}$, $C_o = 0.1\text{F}$, unless otherwise specified)

| Parameter | Symbol | Test conditions | Min | Typ | Max | Unit | |
|--------------------------|--------------|---|---|-------|--------|-------------------|---|
| Output Voltage | V_o | 25°C | -11.52 | -12 | -12.48 | V | |
| | | 0-125 $^\circ\text{C}$ | $-14.5\text{V} \leq V_i \leq -27\text{V}$, $I_o = 1\text{mA} \sim 40\text{mA}$ | -11.4 | -12 | -12.6 | V |
| | | | $I_o = 1\text{mA} \sim 70\text{mA}$ | -11.4 | -12 | -12.6 | V |
| Load Regulation | ΔV_o | $I_o = 1\text{mA} \sim 100\text{mA}$, 25°C | | 24 | 100 | mV | |
| | | $I_o = 1\text{mA} \sim 40\text{mA}$, 25°C | | 15 | 50 | mV | |
| Line Regulation | ΔV_o | $-14.5\text{V} \leq V_i \leq -27\text{V}$, 25°C | | 50 | 250 | mV | |
| | | $-16\text{V} \leq V_i \leq -27\text{V}$, 25°C | | 40 | 200 | mV | |
| Quiescent Current | I_q | 25°C | | | 6.5 | mA | |
| Quiescent Current Change | ΔI_q | $-16\text{V} \leq V_i \leq -27\text{V}$, 0-125 $^\circ\text{C}$ | | | 1.5 | mA | |
| | | $1\text{mA} \leq I_o \leq 40\text{mA}$, 0-125 $^\circ\text{C}$ | | | 0.1 | mA | |
| Output Noise Voltage | V_N | 10Hz $\leq f \leq$ 100KHz, 25°C | | 80 | | $\mu\text{V}/V_o$ | |
| Ripple Rejection | RR | $-15\text{V} \leq V_i \leq -25\text{V}$, $f = 120\text{Hz}$, 0-125 $^\circ\text{C}$ | 37 | 42 | | dB | |
| Dropout Voltage | V_d | 25°C | | 1.7 | | V | |

* Pulse test.

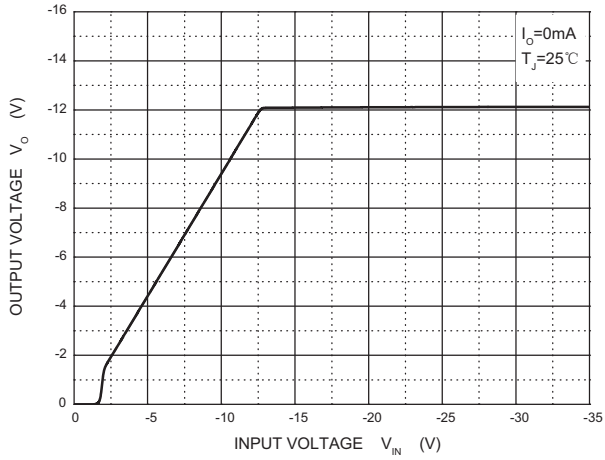
TYPICAL APPLICATION



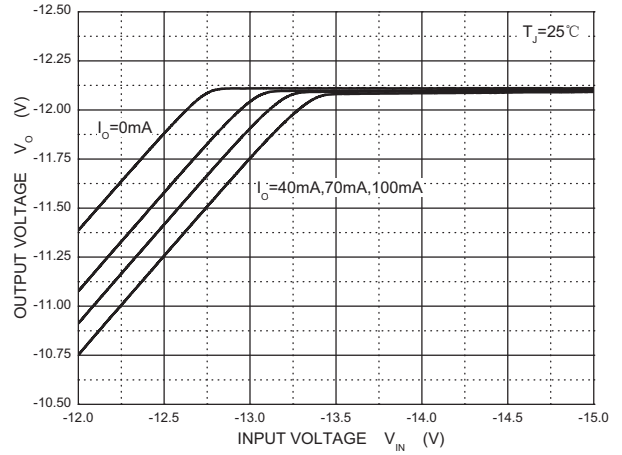
Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

TYPICAL APPLICATION

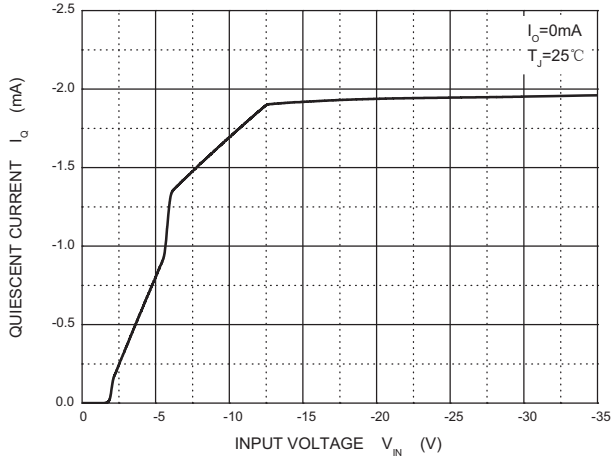
Output Characteristics



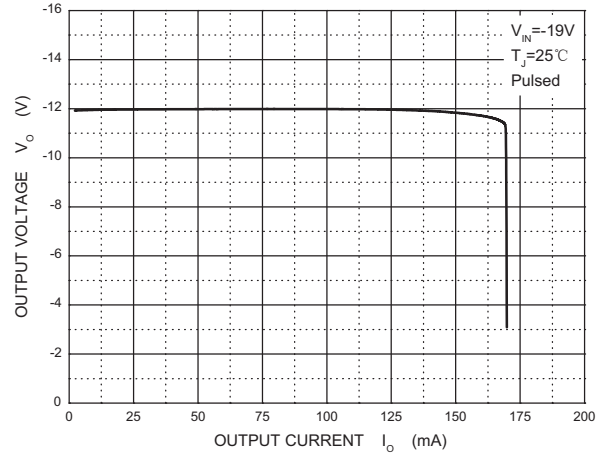
Dropout Characteristics



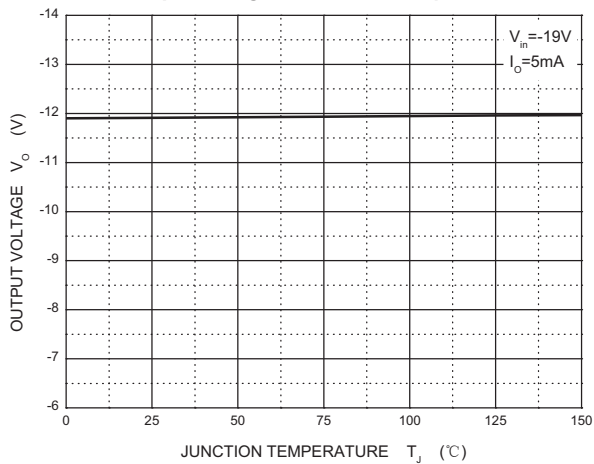
Quiescent Current vs Input Voltage



Current Cut-off Grid Voltage



Output Voltage vs Junction Temperature



Power Derating Curve

